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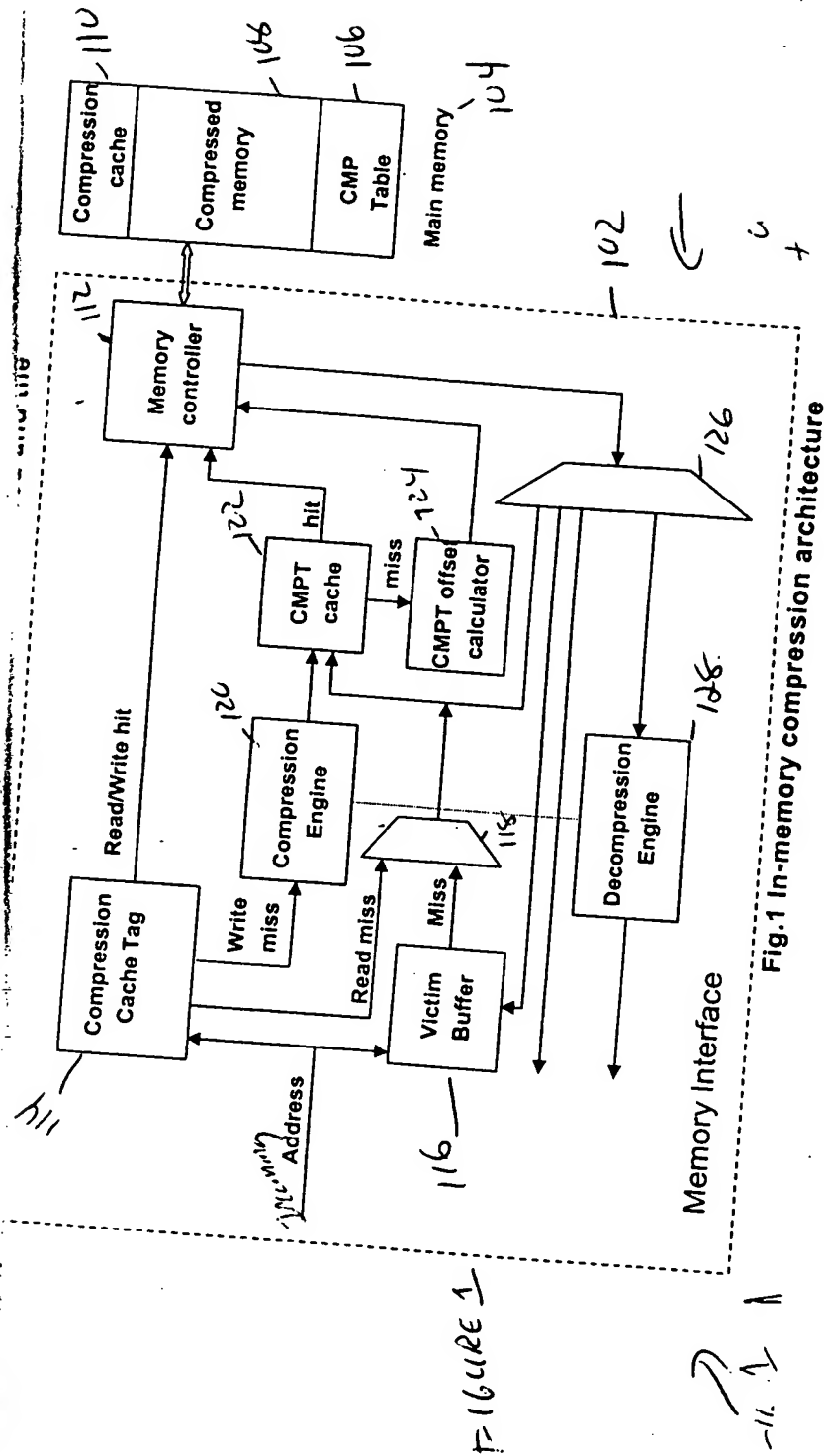
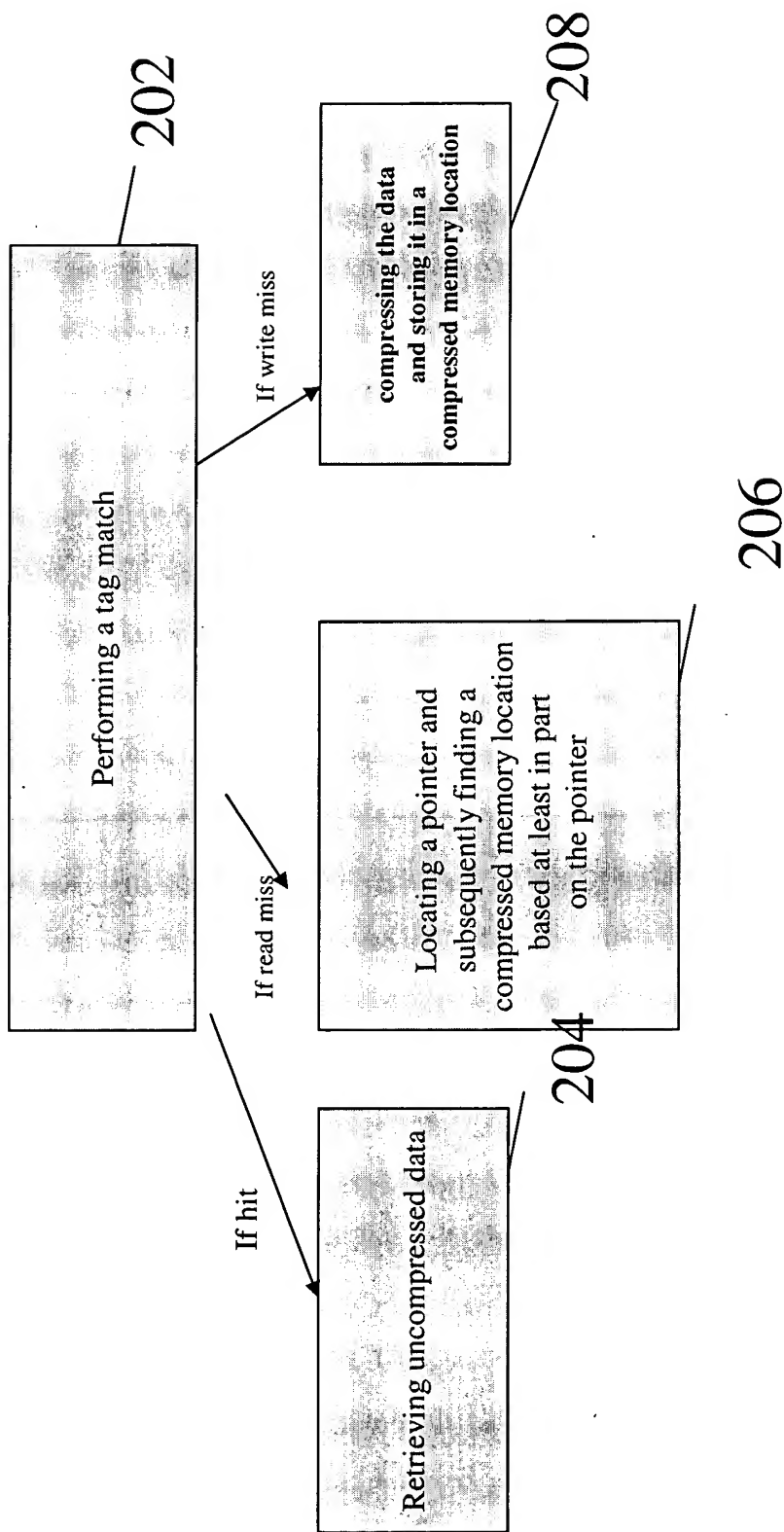


Fig. 1 In-memory compression architecture

Blakely, Sokoloff, Taylor & Zafman LLP
 Title: A METHOD, SYSTEM, AND APPARATUS FOR MEMORY
 COMPRESSION WITH FLEXIBLE IN-MEMORY CACHE
 1st Named Inventor: Siva Ramakrishnan
 Express Mail No.: EV325527887US
 Sheet: 1 of 4
 Docket No.: 42P17216



Blakely, Sokoloff, Taylor & Zafman LLP (503) 684-6200
 Title: A METHOD, SYSTEM, AND APPARATUS FOR MEMORY
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Figure 2

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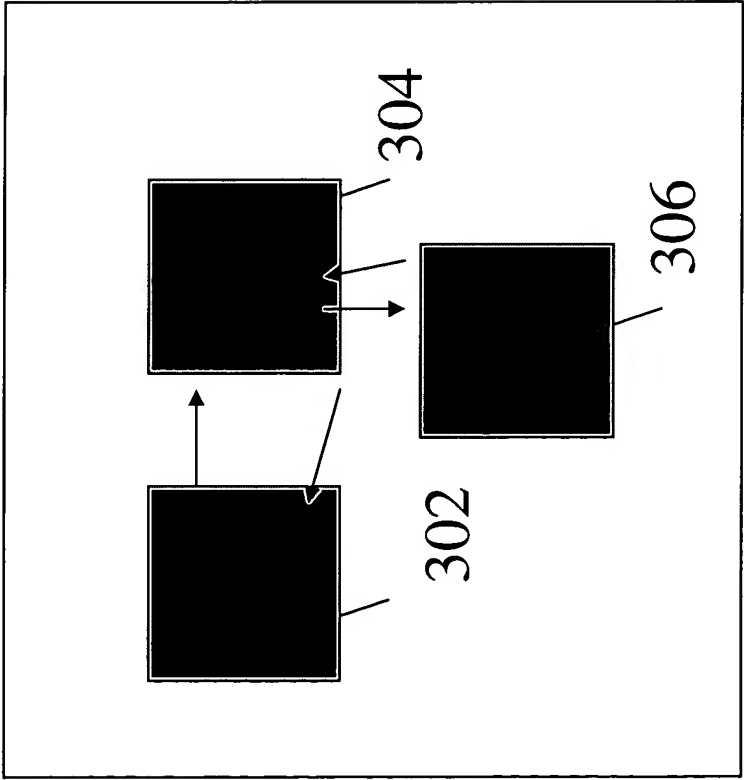


FIGURE 3

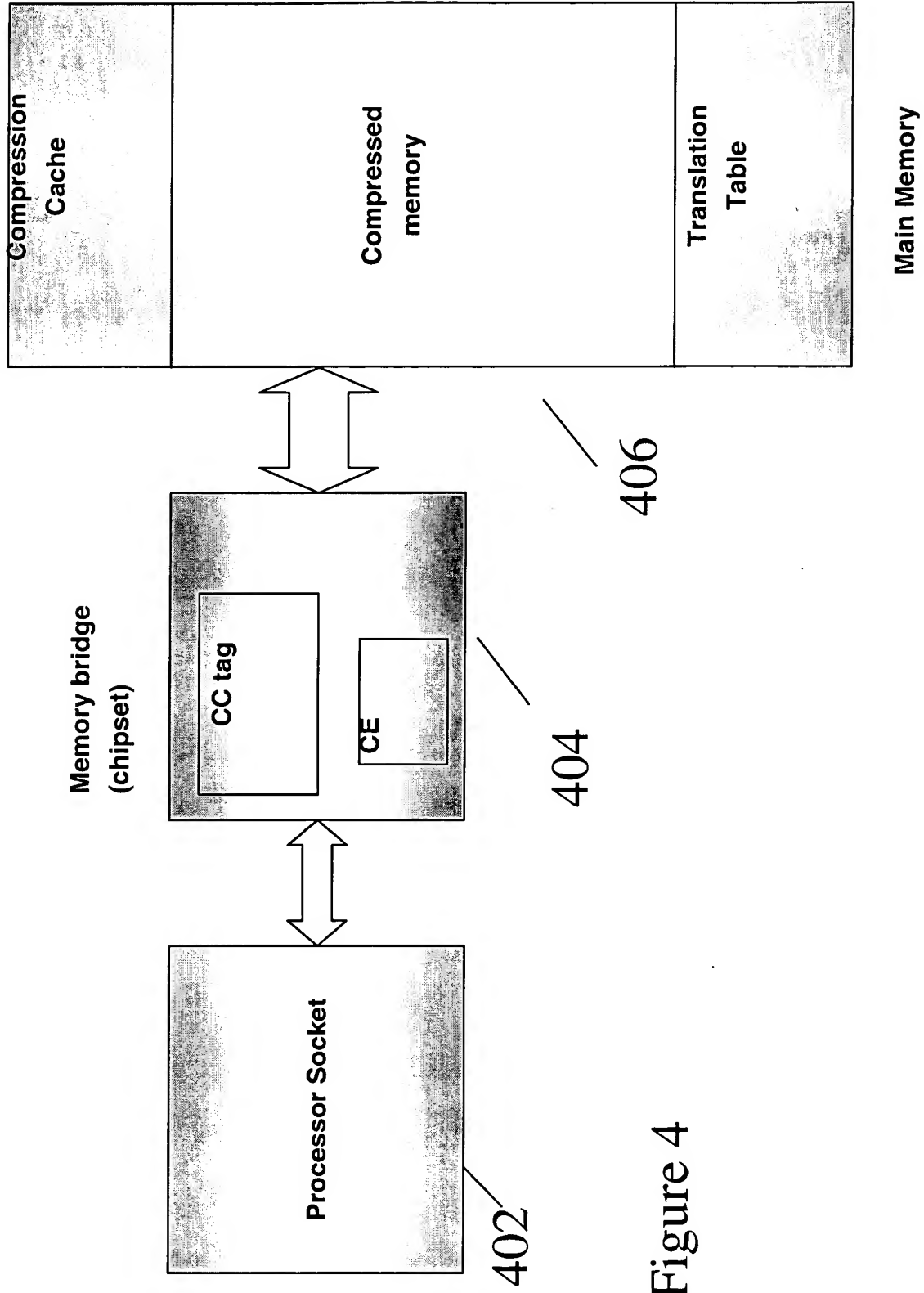


Figure 4